

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	0	((elliptical adj capacitive adj memory) and (first adj electrode) and (electrode with (concentric adj sidewalls)) and (plane with parallel) and substrate). clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/26 19:31
L2	0	((capacitive adj memory) and (first adj electrode) and (electrode with (sidewalls with substrate)) and (perpendicular with substrate) and (parallel with edge)).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/26 19:32
S1	15	("6120952" "6284419" "6319644" "6374396" "6401236" "6418008" "6440612" "6569574").PN.	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/08 15:33
S2	1634	(memory adj cell) and (capacitor with (layout or arrangement))	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/08 15:34
S3	1280	S2 and substrate	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/08 15:34
S4	179	S3 and (slant or angle)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/08 15:34
S5	126	S4 and memory.ab.	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/08 15:35
S6	30	S5 and (layout or arrangement).ab.	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/08 15:35
S7	1	("5045899").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2005/07/08 15:54
S8	2	("6020235").URPN.	USPAT	OR	ON	2005/07/08 16:11
S9	77	S3 and diagonal	USPAT	OR	ON	2005/07/08 16:12
S10	80	S3 and diagonal	USPAT; EPO; JPO; DERWENT	OR	ON	2005/07/08 16:12

EAST Search History

S11	28	S10 and (capacitor with diagonal)	USPAT; EPO; JPO; DERWENT	OR	ON	2005/07/08 16:12
S12	335	(film adj deposition adj system).ab.	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/11 09:32
S13	1438	((machine adj readable adj medium) and instruction).ab.	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/11 18:05
S14	4452	dram and substrate and processor	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/11 18:06
S15	5063	dram with processor	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/11 18:06
S16	1160	S14 and dram with processor	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/11 18:06
S17	922	S15 and (dram with processor).ab.	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/11 18:08
S18	23	S17 and ((dram and processor) with substrate)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/11 18:12
S19	0	S17 and (system adj on with chip)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/11 18:12
S20	111	S17 and (system with chip)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/12 09:30
S21	227	257/396.ccls.	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/12 09:32
S22	2629	257/296.ccls.	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/12 09:38

EAST Search History

S23	259	257/68.ccls.	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/12 09:41
S24	173	257/71.ccls.	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/12 09:44
S25	561	257/300.ccls.	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/12 09:47
S26	1578	257/532.ccls.	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/12 09:47